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~~Q. 6.28: Design a counter with the following repeated binary sequence 0, 1, 2, 4, 6 Use D flip-flops~~ Digital Design: Q. 1.13: Do the following conversion problems: (a) Convert decimal 27.315 to binary ~~Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y; and one output z is~~ Q. 3.28: Derive the circuits for a three-bit parity generator and four-bit parity checker using an ~~Q. 7.9: A DRAM chip uses two dimensional address multiplexing. It has 13 common address pins, with t~~ Q. 2.15: Simplify the following Boolean functions T1 and T2 to a minimum number of literals. Q.

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2.12: We can perform logical operations on strings of bits by considering each pair of correspond Q. 4.36: An 8×1 multiplexer has inputs A, B, and C connected to the selection inputs S_2 , S_1 , and S_0 Q. 4.27: A combinational circuit is specified by the following three Boolean functions Q. 4.4: Design a combinational circuit with three inputs and one output. (a) The output is 1 when Digital Design: Q: 1.6: The solutions to the quadratic equation $x^2 - 11x + 22 = 0$ are $x = 3$ and $x = 6$. Q. 2.19: Express following function as sum of minterms and product of maxterms: $F = B'D + A'D + BD$
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Digital Design Q. 1.11: Perform the following division in binary: $111011 \div 101$ Q. 3.15: Simplify the following Boolean function F, together with the don't-care conditions d, and Q. 3.9: Find all the prime implicants for the following Boolean functions, and determine which are Q.

4.8: Design a code converter that converts a decimal digit from the 8, 4, -2, -1 code to BCD Q. 4.1: Consider the combinational circuit shown in Fig. P4.1.(a)* Derive the Boolean expressions for Q. 3.12: Simplify the following Boolean functions to product-of-sums form: (a) $F(w,x,y,z) = \sum(0,1,2, Q. 2.16: The logical sum of all minterms of a Boolean function of n variables is 1. (a) Prove the$

Exercise Solution - Chapter # 1

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(Part-1) - Digital and logic design |
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Q. 3.16: Simplify the following functions, and implement them with two-level NAND gate circuits:

Q. 7.10: Given the 8-bit data word 01011011, generate the 13-bit composite word for the Hamming code

Q. 2.10: Given the Boolean functions F_1 and F_2 , show that (a) The Boolean function $E = F_1 + F_2$ cont

Q 7.28: Draw a PLA circuit to implement the functions F_1

$= A'B + AC + A'BC'$ F_2

$= (AC + AB + BC)'$ Q. 3.18: Draw a

logic diagram using only two-input NOR gates to implement the following function: Q. 1.1: List the

octal and hexadecimal numbers from 16 to 32. Using A and B for the last two digits Computer Logic

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~~Digital Design M Morris Mano Part 2
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For a deck with 52 cards, we need
6 bits ($32 < 52 < 64$). Let the
msb's select the suit (e.g.,
diamonds, hearts, clubs, spades
are encoded respectively as 00,
01, 10, and 11. The remaining
four bits select the "number" of
the card.

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Illustrates how and why Verilog HDL is used to develop today's most complex digital designs. This book is valuable to both the novice and the experienced Verilog user. I highly recommend it to anyone exploring Verilog-based design."

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This textbook covers digital design, fundamentals of computer architecture, and assembly language. The book starts by introducing basic number systems, character coding, basic knowledge in digital design, and components of a computer. The book goes on to discuss information representation in computing; Boolean algebra and logic gates; sequential logic; input/output; and CPU performance. The author also covers ARM architecture, ARM instructions and ARM assembly language which is used in a variety of devices such as cell phones, digital TV, automobiles, routers, and switches. The book contains a set of laboratory experiments related to digital

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